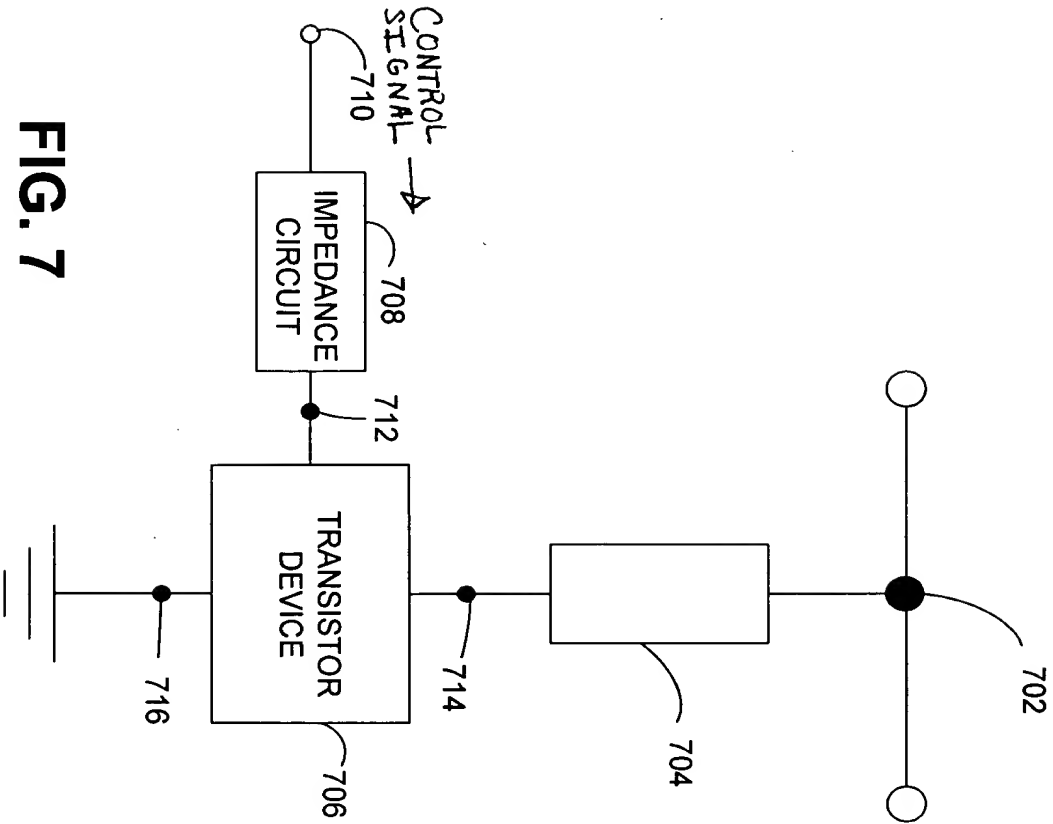


COPY OF PAPERS
ORIGINALLY FILED



FIG. 7



RECEIVED
700
AUG 20 2002
TECHNOLOGY CENTER 2800

COPY OF PERS
ORIGINALLY FILED

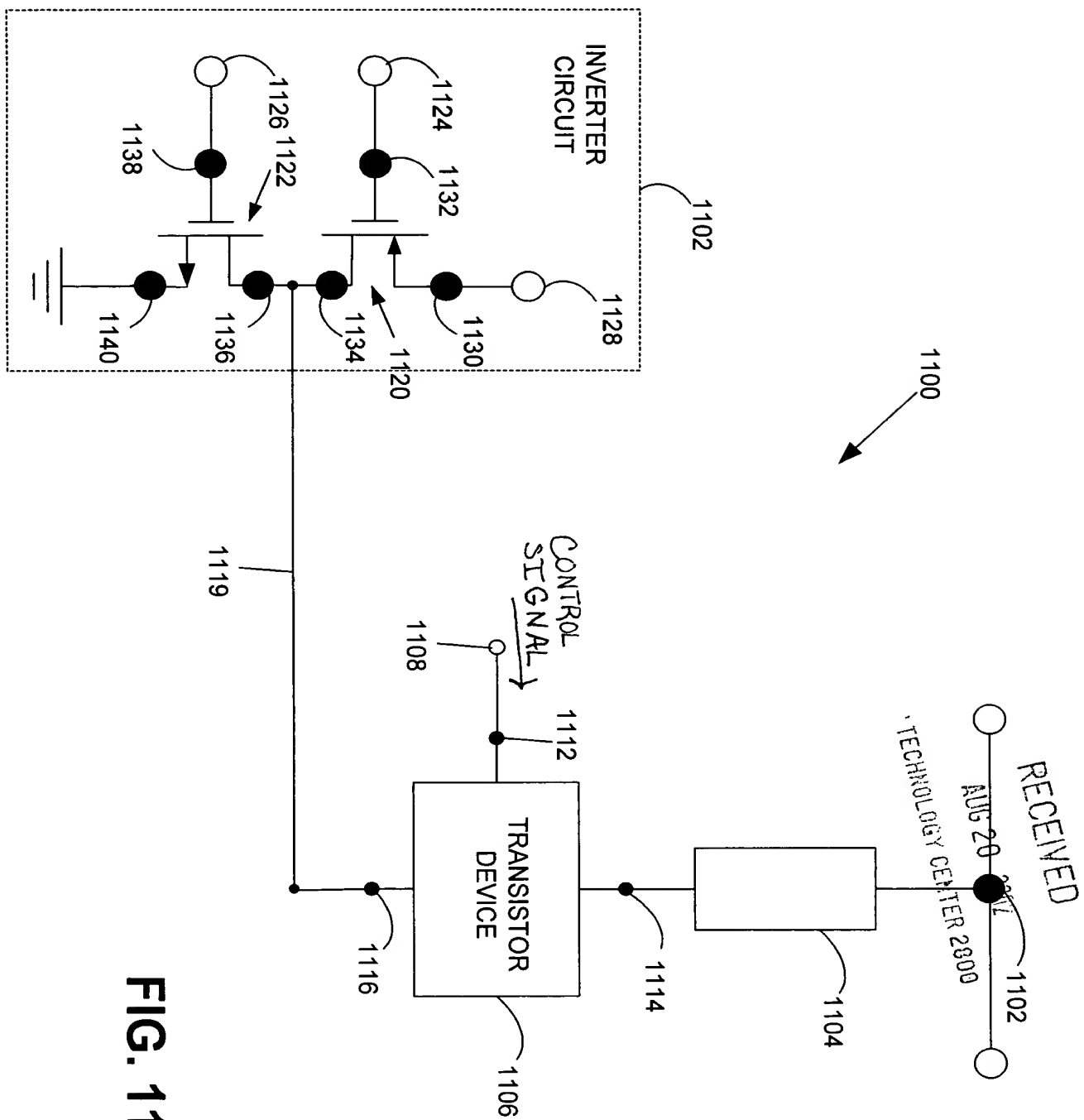


FIG. 11

AUG 20 2002

The diagram illustrates a circuit with two control signals, **CONTROL SIGNAL** (top) and **CONTROL SIGNAL** (bottom). The circuit consists of three transistors: **TRANSISTOR DEVICE 1210**, **TRANSISTOR DEVICE 1212**, and **TRANSISTOR DEVICE 1220**. The gates of transistors 1210 and 1212 are connected to the top **CONTROL SIGNAL** through nodes 1214 and 1222. The gates of transistors 1210 and 1220 are connected to the bottom **CONTROL SIGNAL** through nodes 1240 and 1244. The source of transistor 1210 is connected to node 1224, which is also the source of transistor 1220. The source of transistor 1212 is connected to node 1234, which is also the source of transistor 1220. The drain of transistor 1210 is connected to node 1226, which is also the drain of transistor 1220. The drain of transistor 1212 is connected to node 1230, which is also the drain of transistor 1220. The circuit includes two input/output blocks: block 1206 (top) and block 1208 (bottom). Block 1206 is connected to nodes 1202 and 1204, which are connected to nodes 1214 and 1222. Block 1208 is connected to nodes 1230 and 1234, which are connected to nodes 1216 and 1220. The circuit is labeled with the date **AUG 20 2002** and the text **TECHNOLOGY CENTER 2800**.

FIG. 12



COPY OF PAPERS
ORIGINALLY FILED

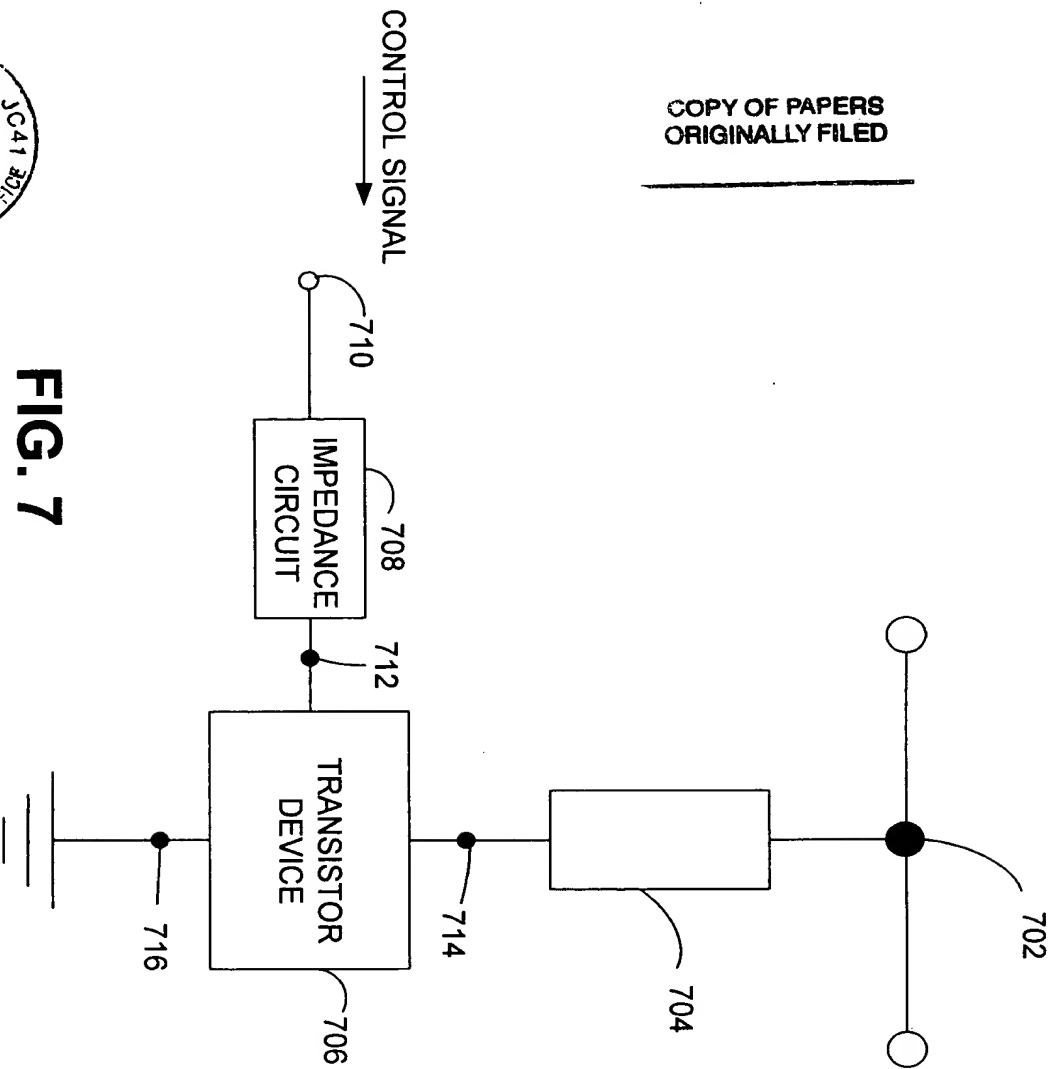


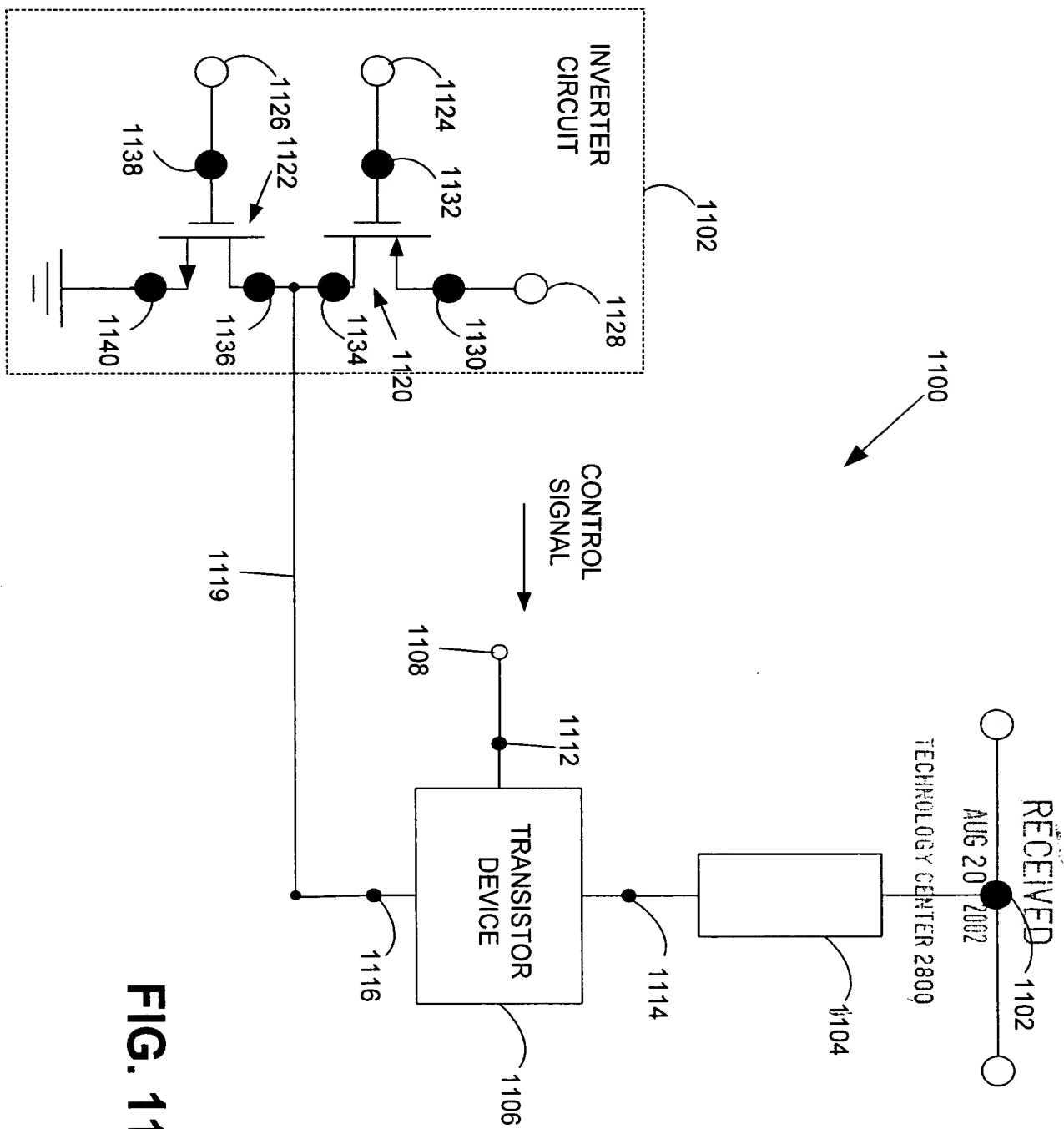
FIG. 7



RECEIVED
AUG 20 2002
TECHNOLOGY CENTER 2800

COPY OF PAPERS
ORIGINALLY FILED

AUG 05 2002



COPY OF PAPERS
ORIGINALLY FILED

1200



CONTROL
SIGNAL



CONTROL
SIGNAL

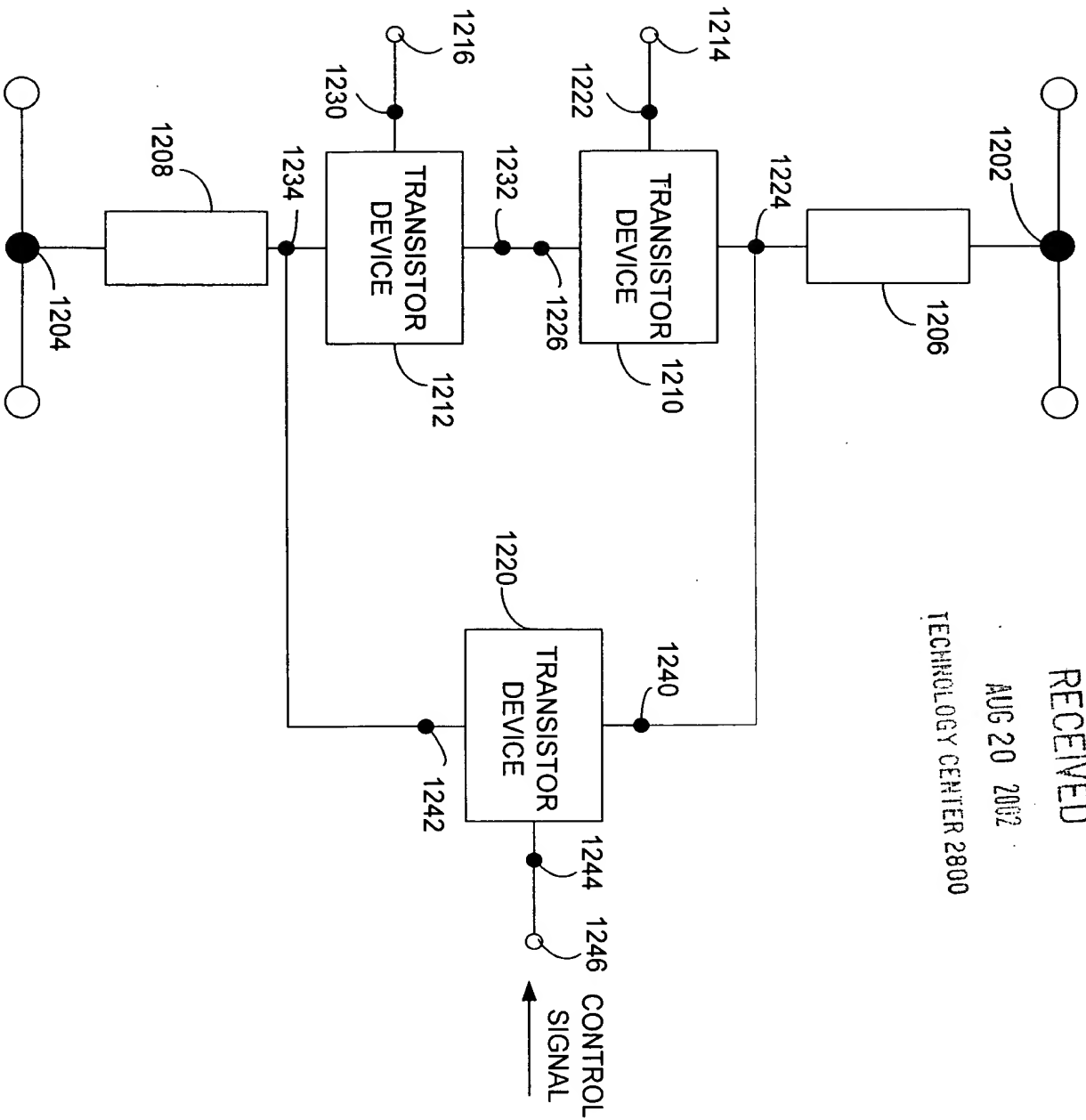


FIG. 12



RECEIVED

AUG 20 2002

TECHNOLOGY CENTER 2800